

ANALYZERS

LAP-C-16032 16 Channel PC based Logic Analyzer

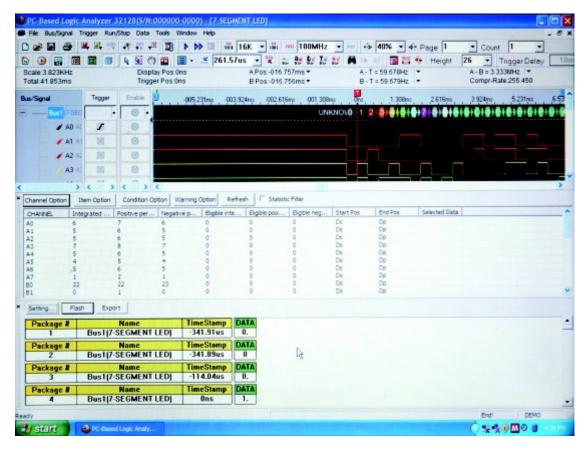


DEBUG CAN NEVER BE SIMPLER!

The analysis done by the Logical Analyzer allows for direct display of the error messages, greatly decreasing the debug time by quickly discovering the problem. The embedded design of the system allows to track errors in test systems such as system collapse, intermittent errors etc.

OBTAINING DIGITAL SIGNALS CAN NEVER BE EASIER!

Logical Analyzer facilitates the analysis of digital signals, observation of multi-channel logical messages, and the examination of the logical status as well as the corrections of the time sequence.









PROTOCOL ANALYZER SUPPORTS

I2C, UART, SPI, 7-segment

Arithmetic Logic, Digital Logic, JK Flip Flop, Up Down counter (LAP-C-16032)

FEATURES

- 16 Channel
- Portable Logic Analyzer
- User Interface
- Memory 512Kbits
- · Supports Protocol Analysis
- Hook up with Bench top tools like oscilloscope etc.

SPECIFICATIONS

Operating Systems : Windows 2000 / XP / Vista

Interface : USB2.0 (1.1)
Sample Rate - Internal Clock (Timing Mode) : 100Hz~100MHz

Sample Rate - External Clock (State Mode) : 75MHz

THRESHOLD VOLTAGES

Bandwidth : 75 MHzWorking Range : $-6 \text{V} \sim +6 \text{V}$ Accuracy : $\pm 0.1 \text{V}$

MEMORY

Length : 512Kbits

Depth (per channel) : 32Kbits (8Mbits for compression for LAP-C-16032)

TRIGGER

Condition : Pattern/Edge

Channel : 16 CH
Pre/Post Trigger : YES
Level : 1 Level
Count : 1~65535

GENERAL

Power : USB

Safety Certification : FCC/CE/WEEE/RoHS/SVHS

Working Voltage 4.5V - 5.5VCurrent at Rest 200mA Current at Work 400mA Power at Rest 1W Power at Work 2W +/- 1.5ns Error in Phase OFF Impedance 500KO/10pF Vinput of Testing Channels -30V DC - 30V DC

Viright of resting chainless -60 DC - 60 DC Verference -60 DC - 60 DC Working Temperature -60 C - 60 C Storage Temperature -40 C - 80 C

ACCESSORIES : 16 channel testing cable, Probes,

Power cord, Instruction Manual, Software CD

