

ANALYZERS

LAP-C-32128 32 Channel PC based Logic Analyzer

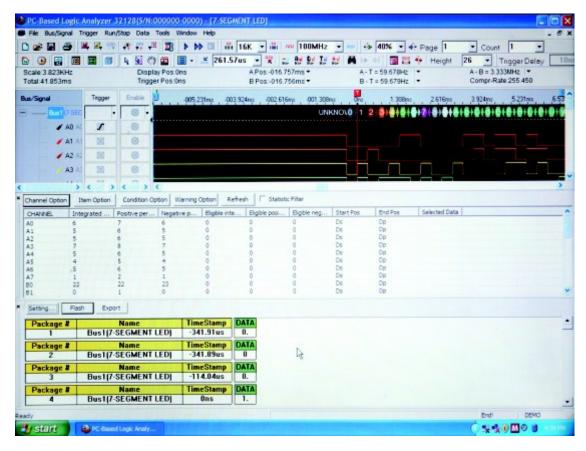


DEBUG CAN NEVER BE SIMPLER!

The analysis done by the Logical Analyzer allows for direct display of the error messages, greatly decreasing the debug time by quickly discovering the problem. The embedded design of the system allows to track errors in test systems such as system collapse, intermittent errors etc.

OBTAINING DIGITAL SIGNALS CAN NEVER BE EASIER!

Logical Analyzer facilitates the analysis of digital signals, observation of multi-channel logical messages, and the examination of the logical status as well as the corrections of the time sequence.









PROTOCOL ANALYZER SUPPORTS

I²C, UART, SPI, 7-segment

1-wire, MicroWire, SSI, LIN Bus, Manchester, Miller (LAP-C-32128)

FEATURES

- 32 Channel
- Portable Logic Analyzer
- User Interface
- · Memory 4Mbits
- Supports Protocol Analysis
- · Hook up with Bench top tools like oscilloscope etc.

SPECIFICATIONS

Operating Systems : Windows 2000 / XP / Vista

Interface : USB2.0 (1.1)
Sample Rate - Internal Clock (Timing Mode) : 100Hz~200MHz

Sample Rate - External Clock (State Mode) : 100MHz

THRESHOLD VOLTAGES

Bandwidth : 75 MHzWorking Range : $-6 \text{V} \sim +6 \text{V}$ Accuracy : $\pm 0.1 \text{V}$

MEMORY

Length : 4Mbits

Depth (per channel) : 128Kbits (32Mbits for Compression)

TRIGGER

Condition : Pattern/Edge

Channel : 32CH
Pre/Post Trigger : YES
Level : 1 Level
Count : 1~65535

EXTENDING CHANNEL CAPTURE

Channel : 31 CH
Memory : 256Kbits

DOUBLE MODE

Channel : 16 CH RAM Sizes : 256K

GENERAL

Power : USB

Safety Certification : FCC/CE/WEEE/RoHS/SVHS

Working Voltage 4.5V - 5.5VCurrent at Rest 200mA **Current at Work** 400mA 1W Power at Rest Power at Work 2W Error in Phase OFF +/- 1.5ns Impedance 500KO/10pF Vinput of Testing Channels -30V DC - 30V DC Vreference -6V DC - 6V DC 5°C ~70°C. Working Temperature Storage Temperature -40°C ~80°C

ACCESSORIES : 32 /16 channel testing cable, Probes,

Power cord, Instruction Manual, Software CD



